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Method of Fabricating a High-

Voltage Transistor With A Multi-

Layered Extended Drain

Structure

First Named Inventor:

Donald Disney

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> Title of Invention

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| Submitted by: | Elec. Sign. | Sign. Capacity |
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| Bradley John Bereznak Registered Number: 33,474 | BJB | Attorney |

Documents being submitted

Files

us-ids

03692.P059DC-usidst.xml

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Comments



ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18 Stylesheet Version v18.0

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Method of Fabricating a High-Voltage Transistor With A Multi-Layered Extended Drain Structure

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10/722792

Confirmation Number:

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First Named Applicant:

Donald Disney

Attorney Docket Number: 03692.P059DC

Art Unit:

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Examiner:

David Nhu

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20010015459).pn.

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Note: Applicant is not required to submit a paper copy of cited US Patent Documents

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Signature

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